Inventors: Aaron Ferrucci and Todd Wayne
Title: Pseudo-Random Wait-State And Pseudo-Random
Latency Components
Atty. Docket No.: ALTRP112/A1251

Sheet 1 of 8

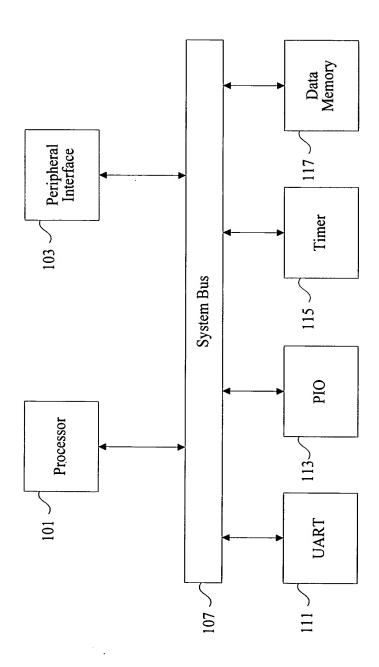


Figure 1

Title: Pseudo-Random Wait-State And Pseudo-Random

**Latency Components** 

Atty. Docket No.: ALTRP112/A1251

Sheet 2 of 8

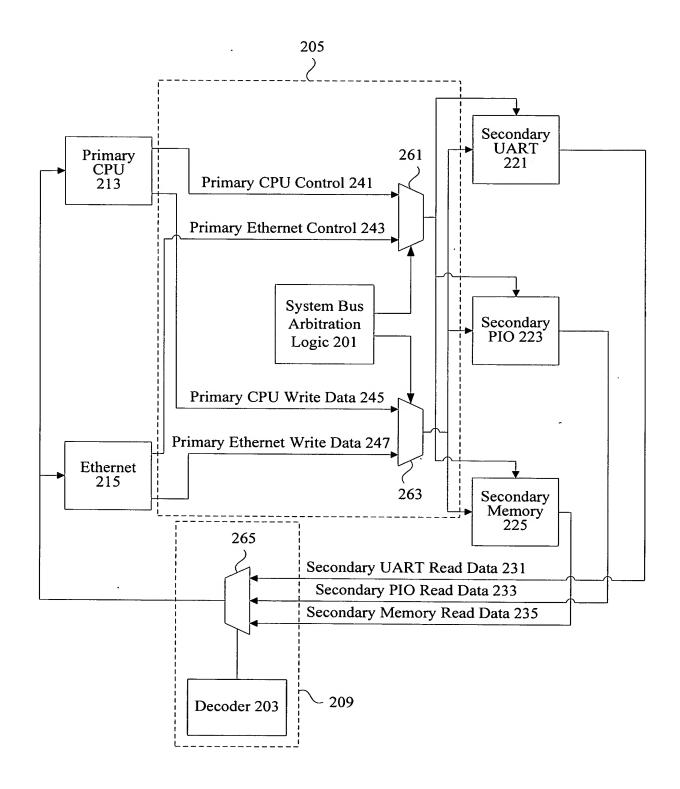
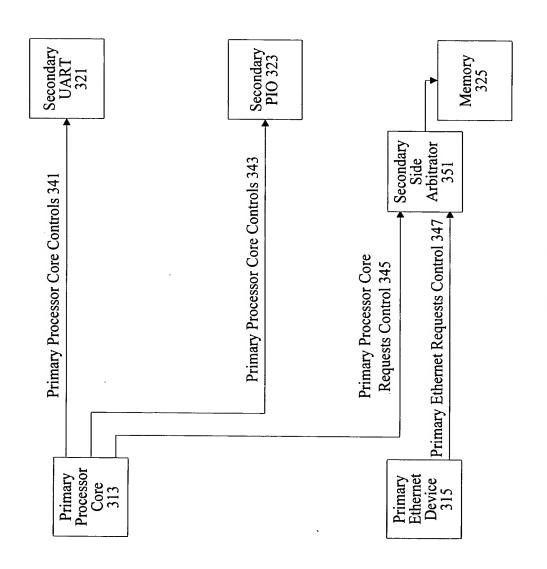


Figure 2

Inventors: Aaron Ferrucci and Todd Wayne
Title: Pseudo-Random Wait-State And Pseudo-Random

Latency Components
Atty. Docket No.: ALTRP112/A1251

Sheet 3 of 8



Inventors: Aaron Ferrucci and Todd Wayne
Title: Pseudo-Random Wait-State And Pseudo-Random

Latency Components
Atty. Docket No.: ALTRP112/A1251

Sheet 4 of 8

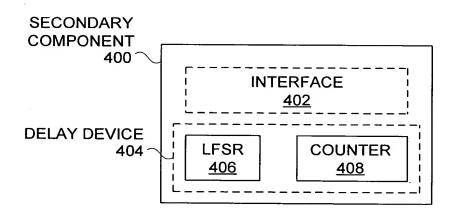


Figure 4

Title: Pseudo-Random Wait-State And Pseudo-Random

**Latency Components** 

Atty. Docket No.: ALTRP112/A1251

Sheet 5 of 8

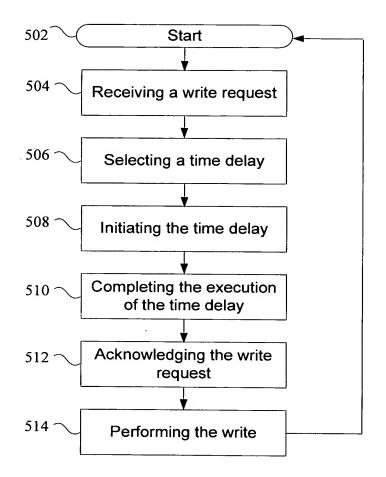


Figure 5

Title: Pseudo-Random Wait-State And Pseudo-Random

**Latency Components** 

Atty. Docket No.: ALTRP112/A1251

Sheet 6 of 8

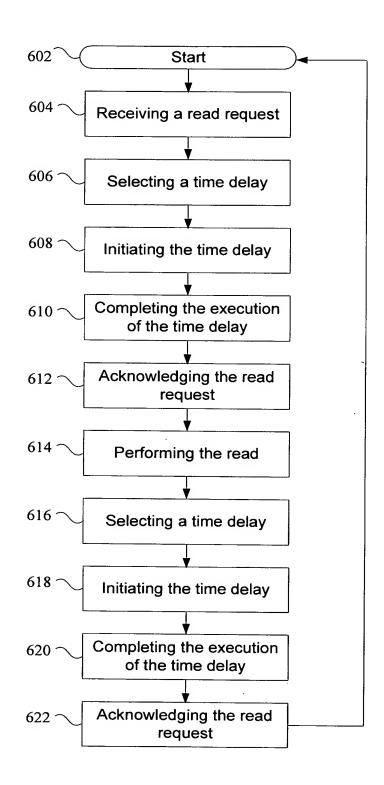


Figure 6

Title: Pseudo-Random Wait-State And Pseudo-Random

Latency Components

Atty. Docket No.: ALTRP112/A1251

Sheet 7 of 8

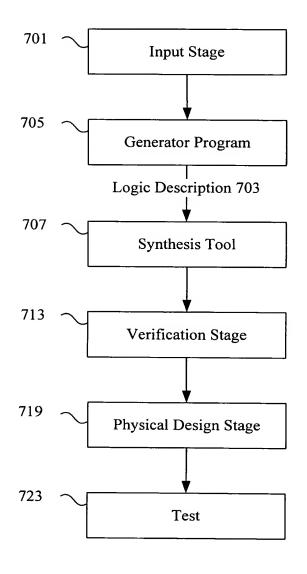


Figure 7

Inventors: Aaron Ferrucci and Todd Wayne
Title: Pseudo-Random Wait-State And Pseudo-Random

**Latency Components** 

Atty. Docket No.: ALTRP112/A1251

Sheet 8 of 8

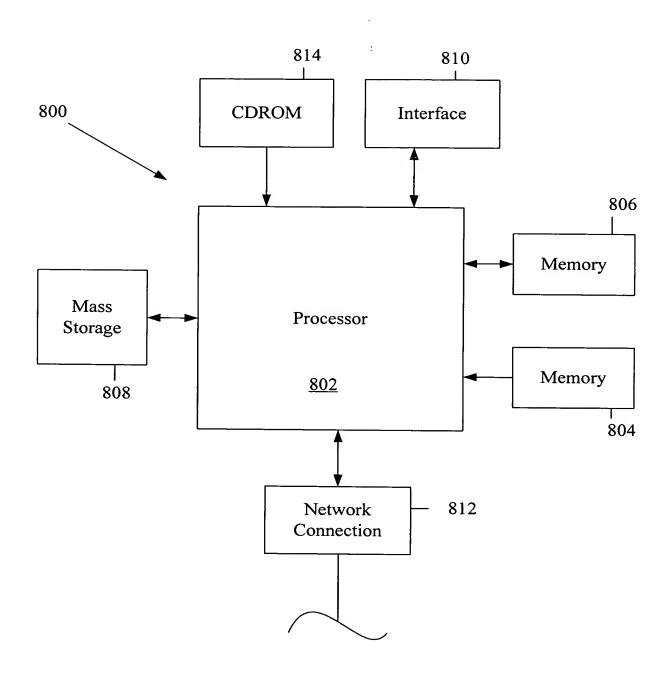


Figure 8